EAST History		2/16/05	10:13 AM
BRS	270532	processing adj units	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
BRS	9269	S1 and pipeline	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
BRS	3015	S2 and ((logic adj unit) or (logic adj circuit))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
BRS	9642	S1 and pipeline	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
BRS	3071	S4 and ((logic adj unit) or (logic adj circuit))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
BRS	2451	S5 and buffer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
BRS	2149	S6 and ((clock-gating adj unit) or (clock-gating adj circuit) or clock)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
BRS	0	S6 and ((clock-gating adj unit) or (clock-gating adj circuit))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
BRS	2149	S6 and clock	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
BRS	252	S6 and (clock adj circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
BRS	9	S9 and ((pipeling adj control adj unit) or (pipeline adj control adj circuit))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
IS&R	122477	(("713") or ("327") or ("326")).CLAS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
BRS	188	S5 and S13	USPAT
BRS	9642	S4 and pipeline	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
BRS	37	S15 and ((pipeling adj control adj unit) or (pipeline adj control adj circuit))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
BRS	19	((clock-gating adj unit) or (clock-gating adj circuit))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
BRS	0	S15 and (pipeline-based adj circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB

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EAST History			2/16/05	10:13 AM
BRS	0	(pipeline-based adj circuit)		US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
BRS	14	(pipeline-based)		US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
BRS	10	S1 and (pipeline-based)		US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
BRS	7	("5918042").URPN.		USPAT